



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#11
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TC 1700

Group Art Unit: 1765
Examiner: Terrence M. Mackey

In Re PATENT APPLICATION Of:

Applicant: Kazutaka SHIBATA

Serial No.: 09/830,092

Filed: June 27, 2001

For: SEMICONDUCTOR DEVICE AND METHOD
FOR MANUFACTURING THE SAME

Orig. Doc. No.: ROH-037

New Doc. No.: AI 264NP

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Examiner's Action dated May 12, 2003, applicant hereby submits one sheet of corrected formal drawings, FIGs. 19 and 20. The formal drawings incorporate the designation of "Prior Art" required by the Examiner on page 2 of the Action.

Respectfully submitted,

Steven M. Rabin - Reg. No. 29,102
RABIN & BERDO, PC
Telephone: 202-371-8976
Facsimile: 202-408-0924
Customer No. 23995

August 6, 2003
Date

SMR:pjl

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